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| Application Number | 10083533 |
| Filing Date | 2002-02-27 |
| First Named Inventor | Hiroshi HASHIMOTO |
| Art Unit | 2814 |
| Examiner Name | Wai Sing Louie

Attorney Docket Number

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| /W.L./ | 1 | International Business Machines. "A single-layer polysilicon EEPROM memory cell using diffusion gate"; January 1, 1999; Research Disclosure, Mason Publications, Hampshire, GB, XP007123866; ISSN: 0374-4353. | | | | | | | | |
|------------|--------|---|------------------------|---------------------|--|--|--|--|--|--|
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